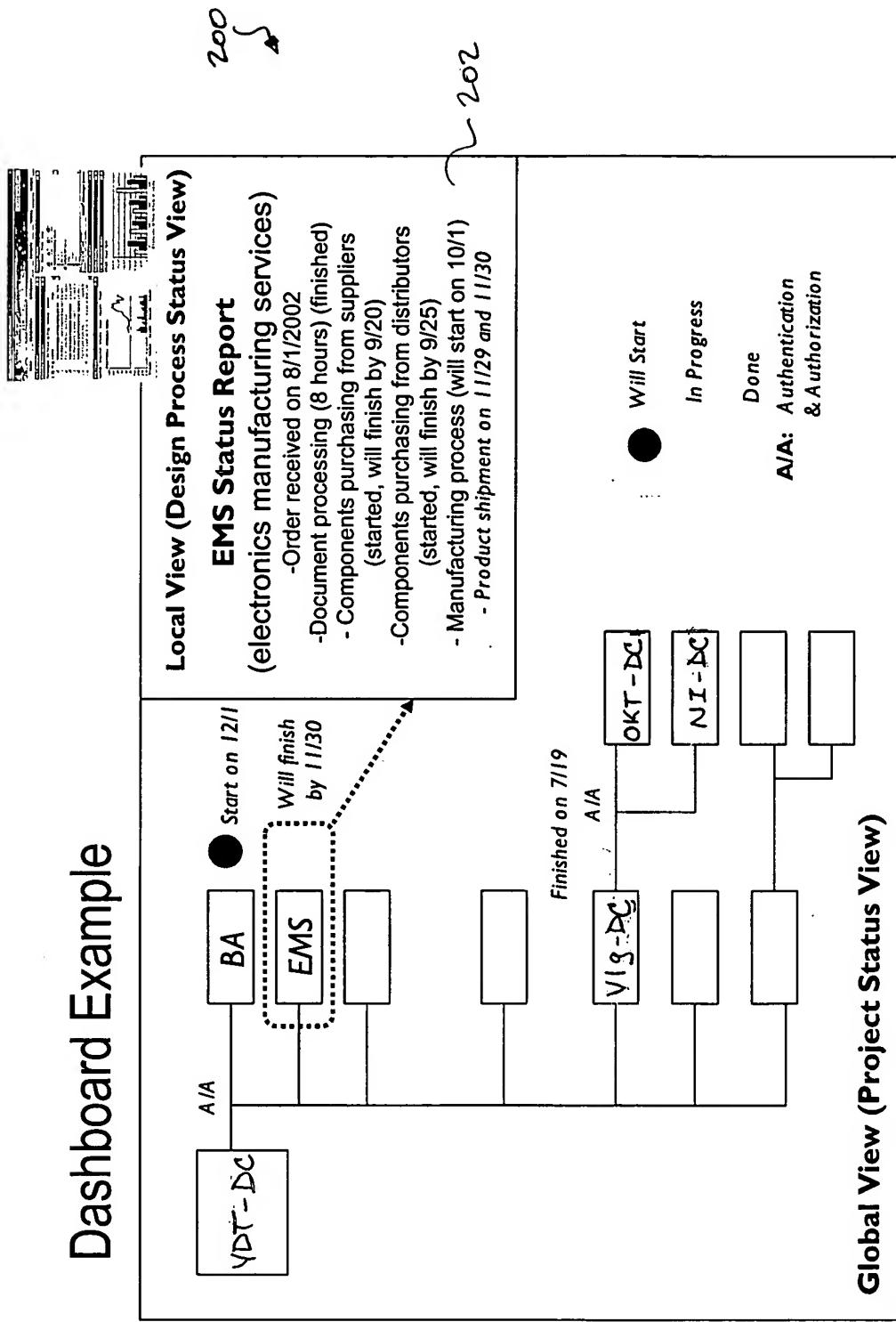


E(6) 1

Dashboard Example



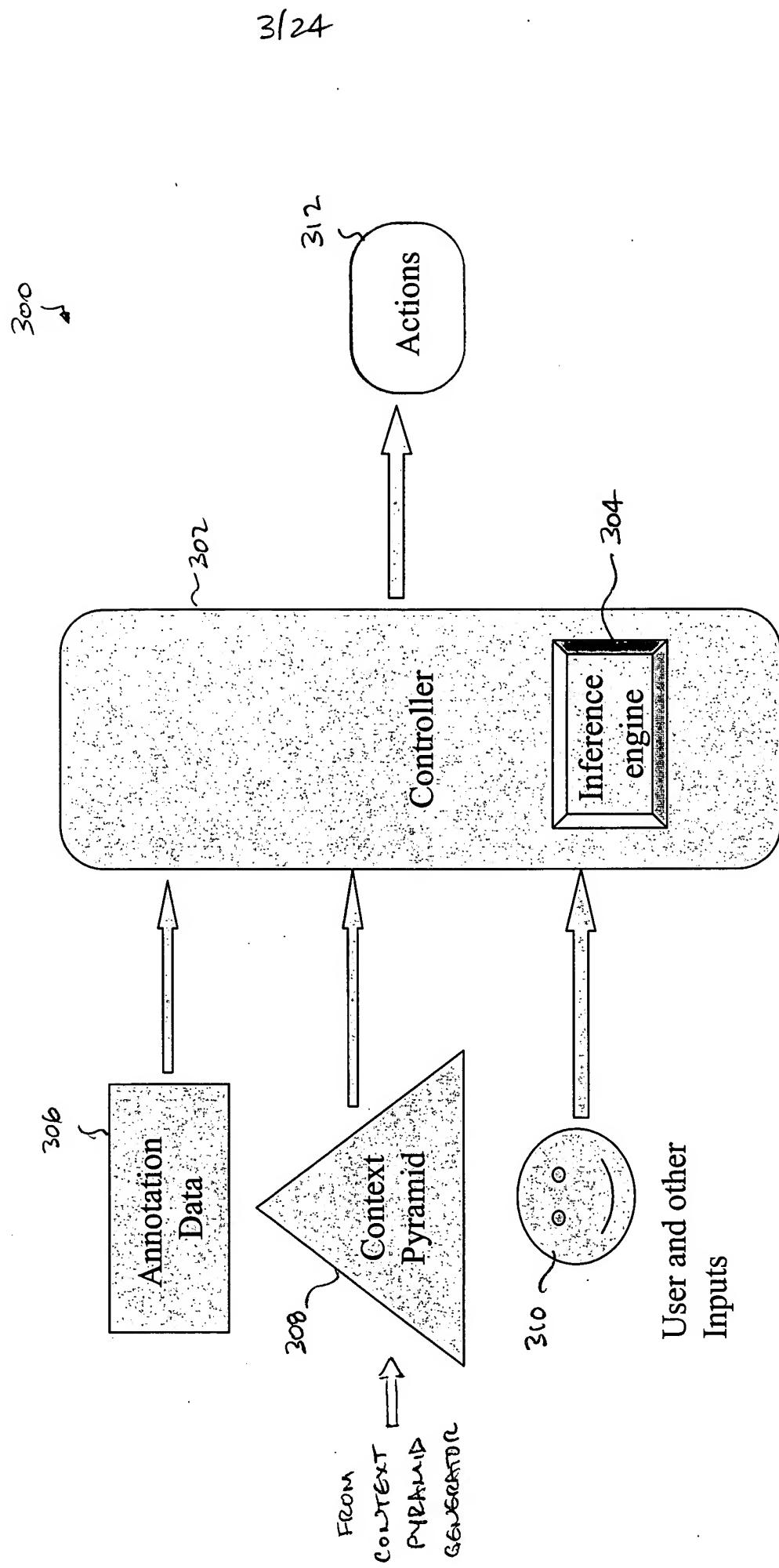
2124

200

202

Fig. 2

FIG. 3



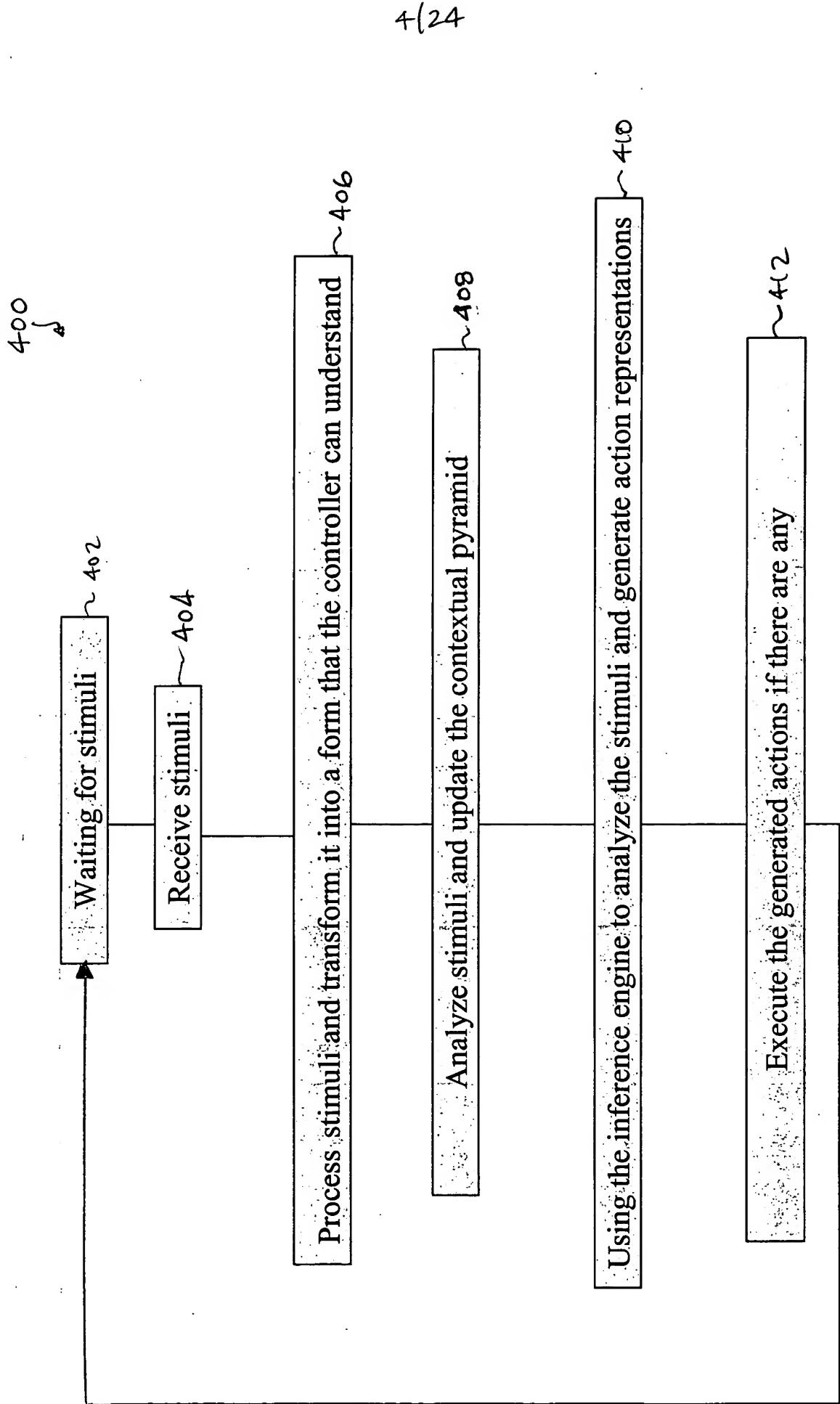
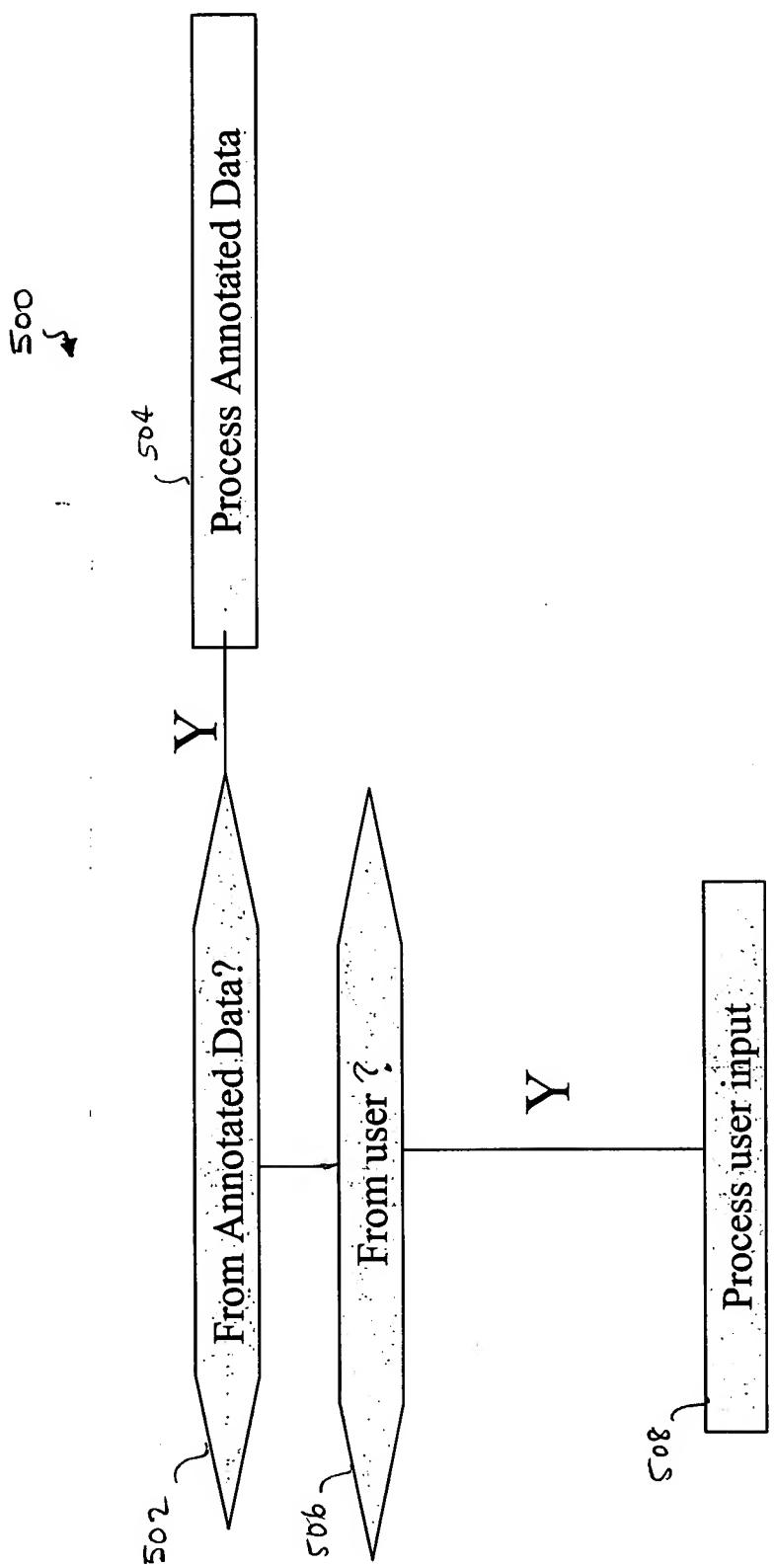
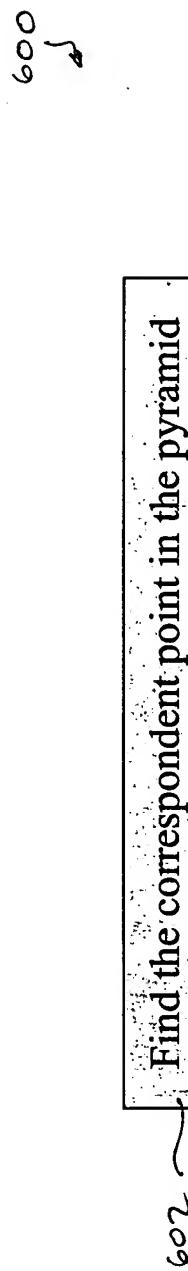


Fig. 4



55



F16. 6

700

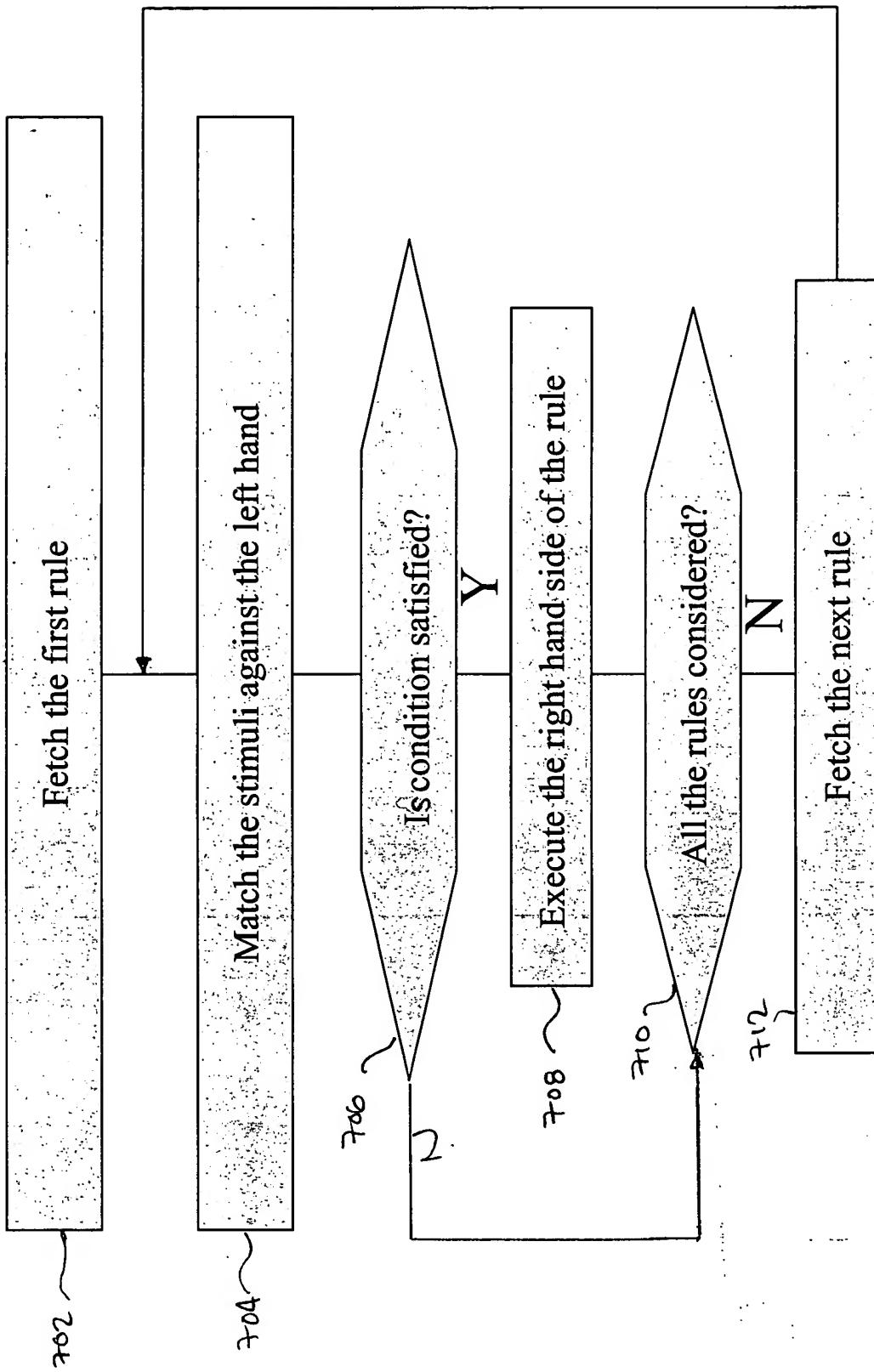
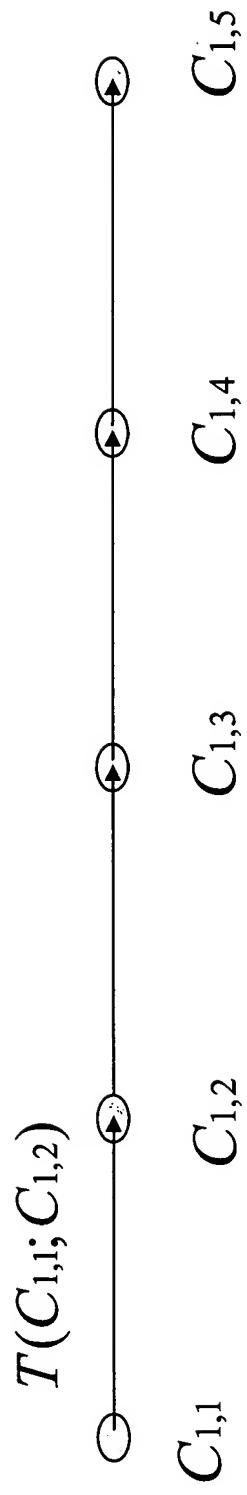


FIG. 7

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F(G)



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FIG. 9

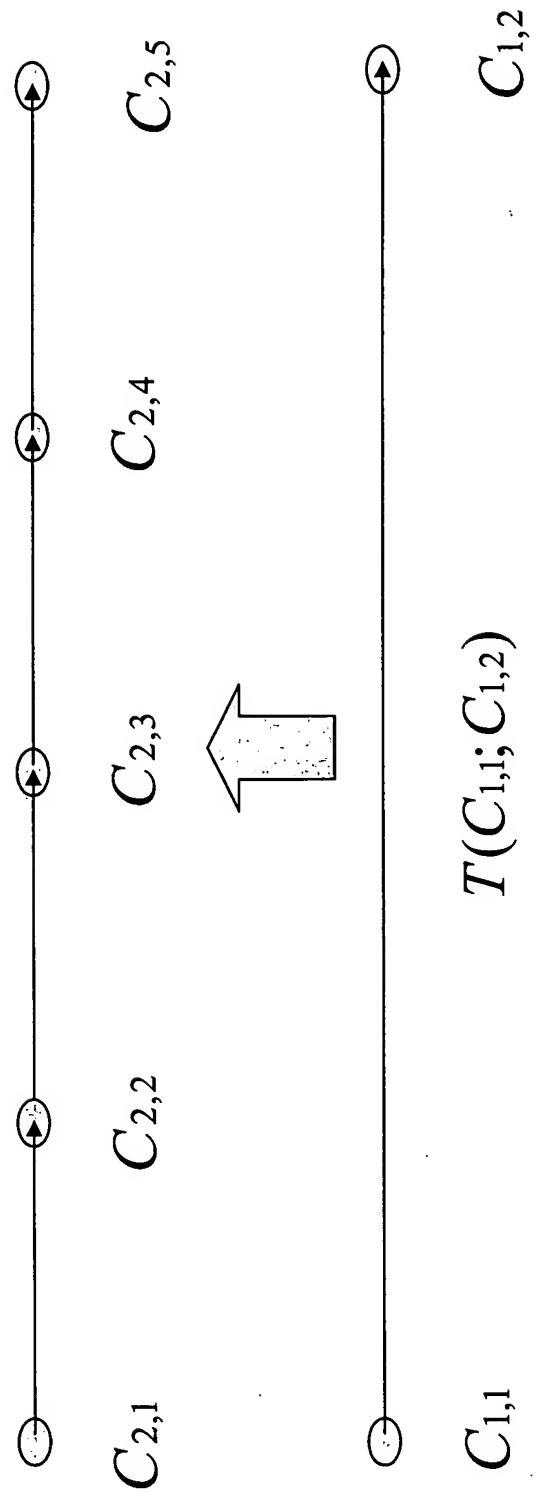
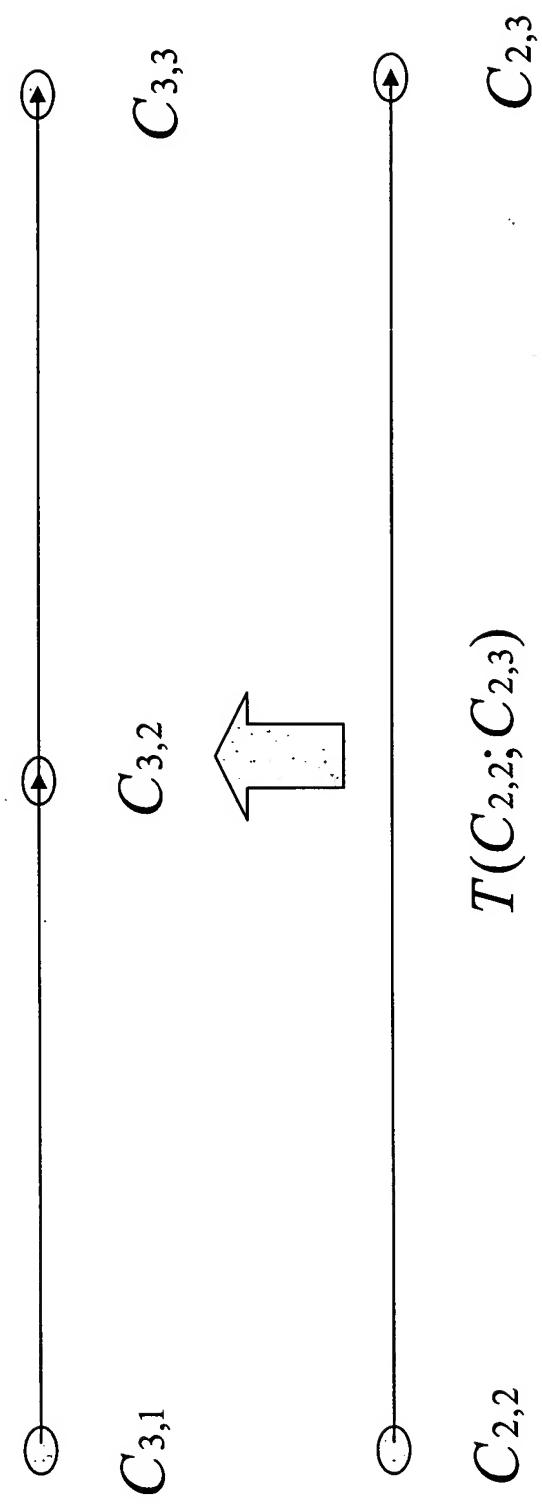
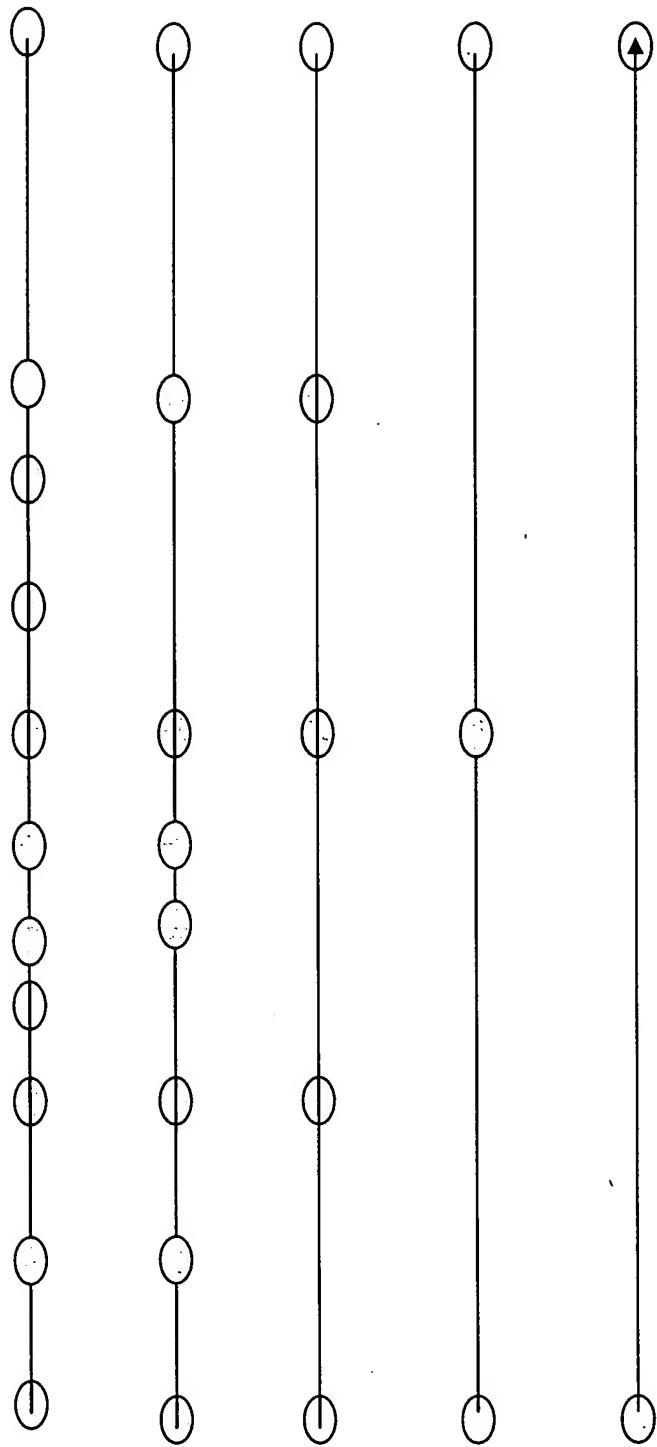


FIG. 10



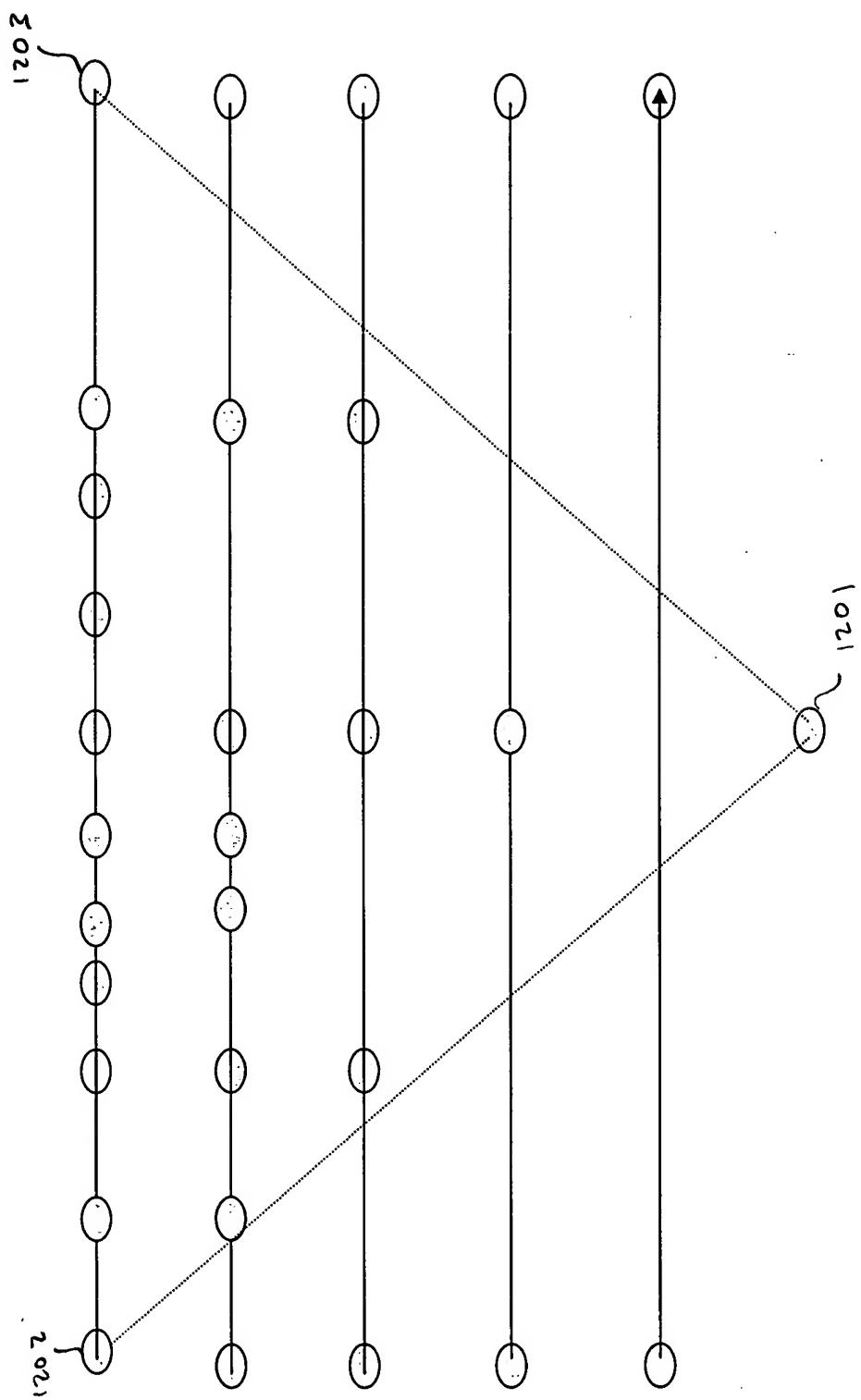
11/24

FIG. =



12/24

FIG. 12



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FIG. 13

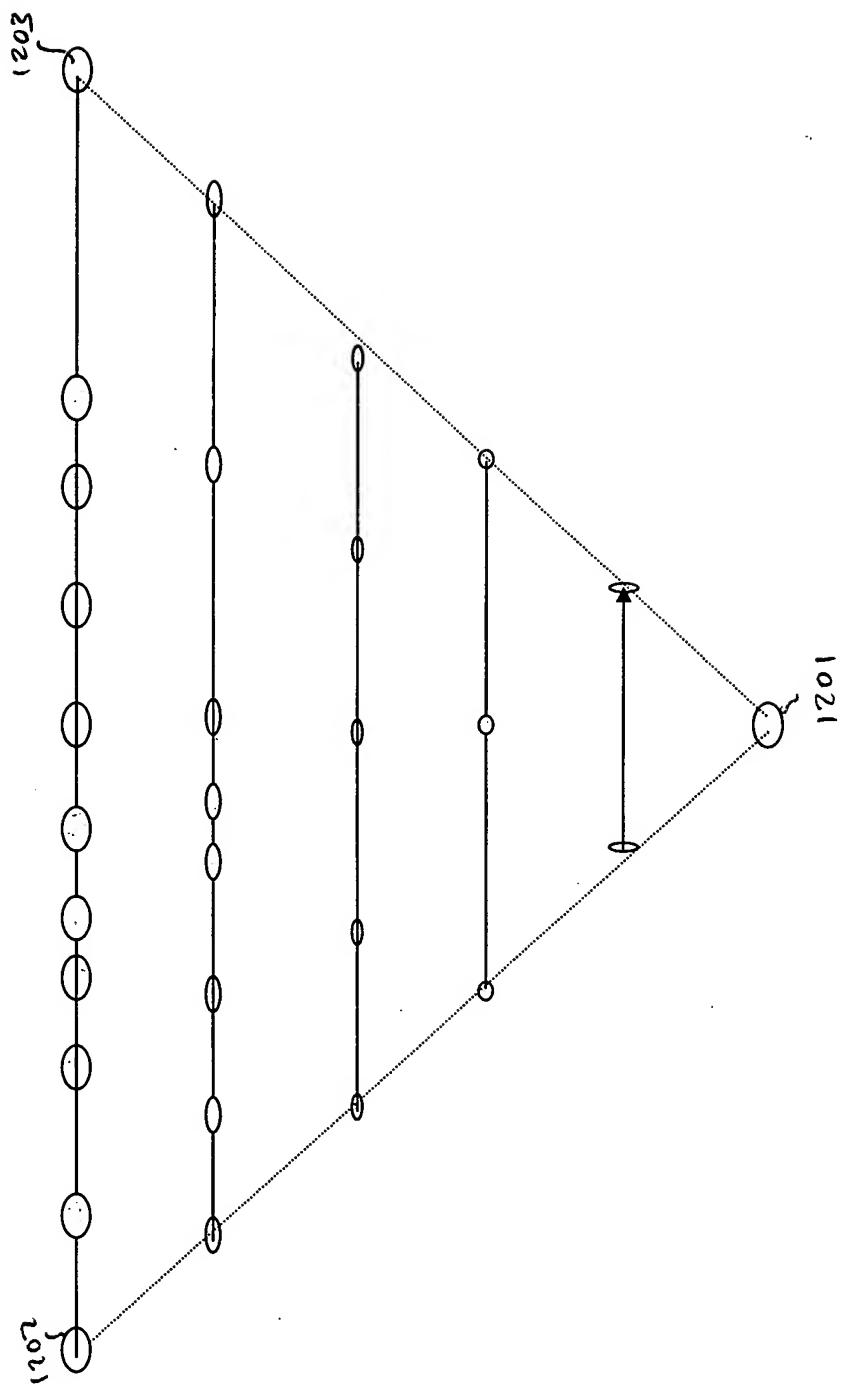


FIG. 14

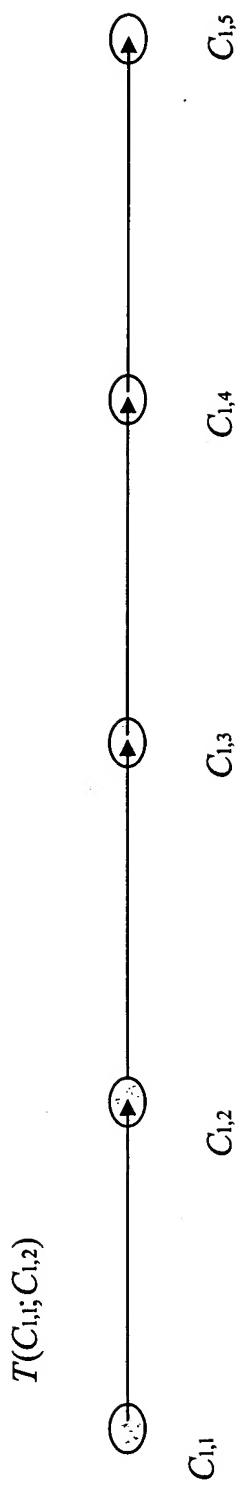
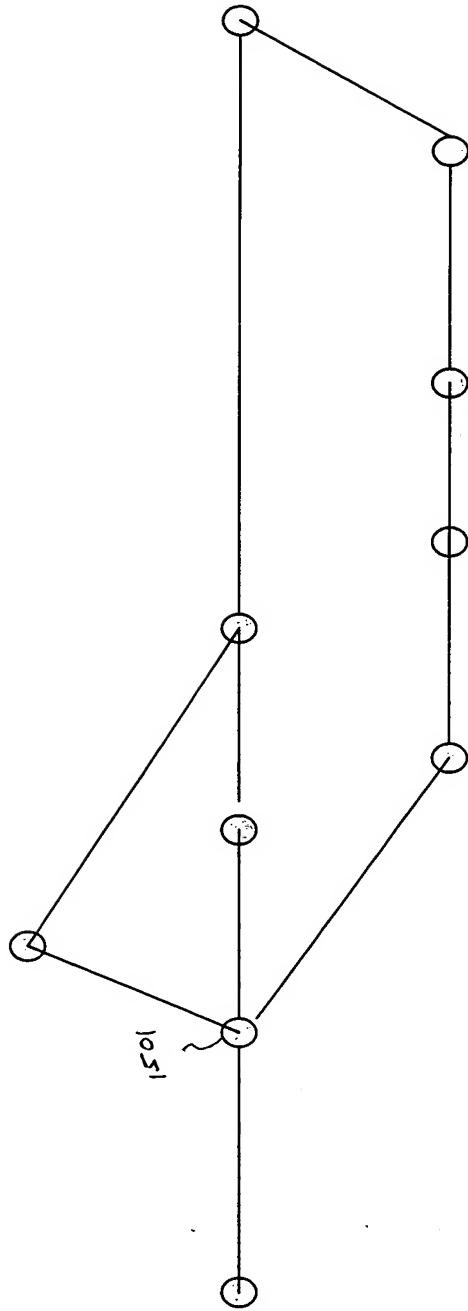


FIG. 15



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1600
↓

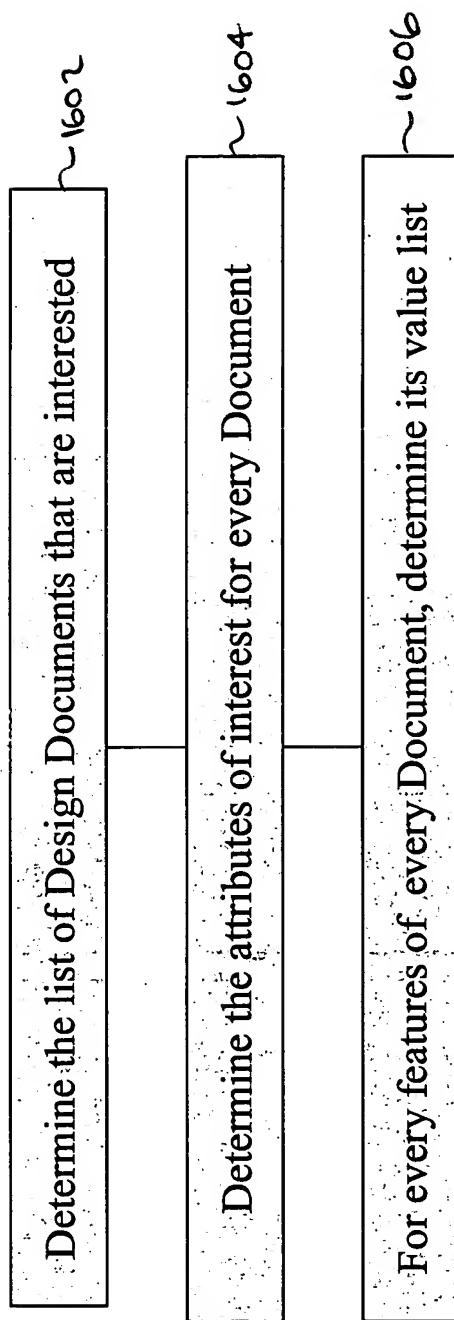


Fig. 16

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1760

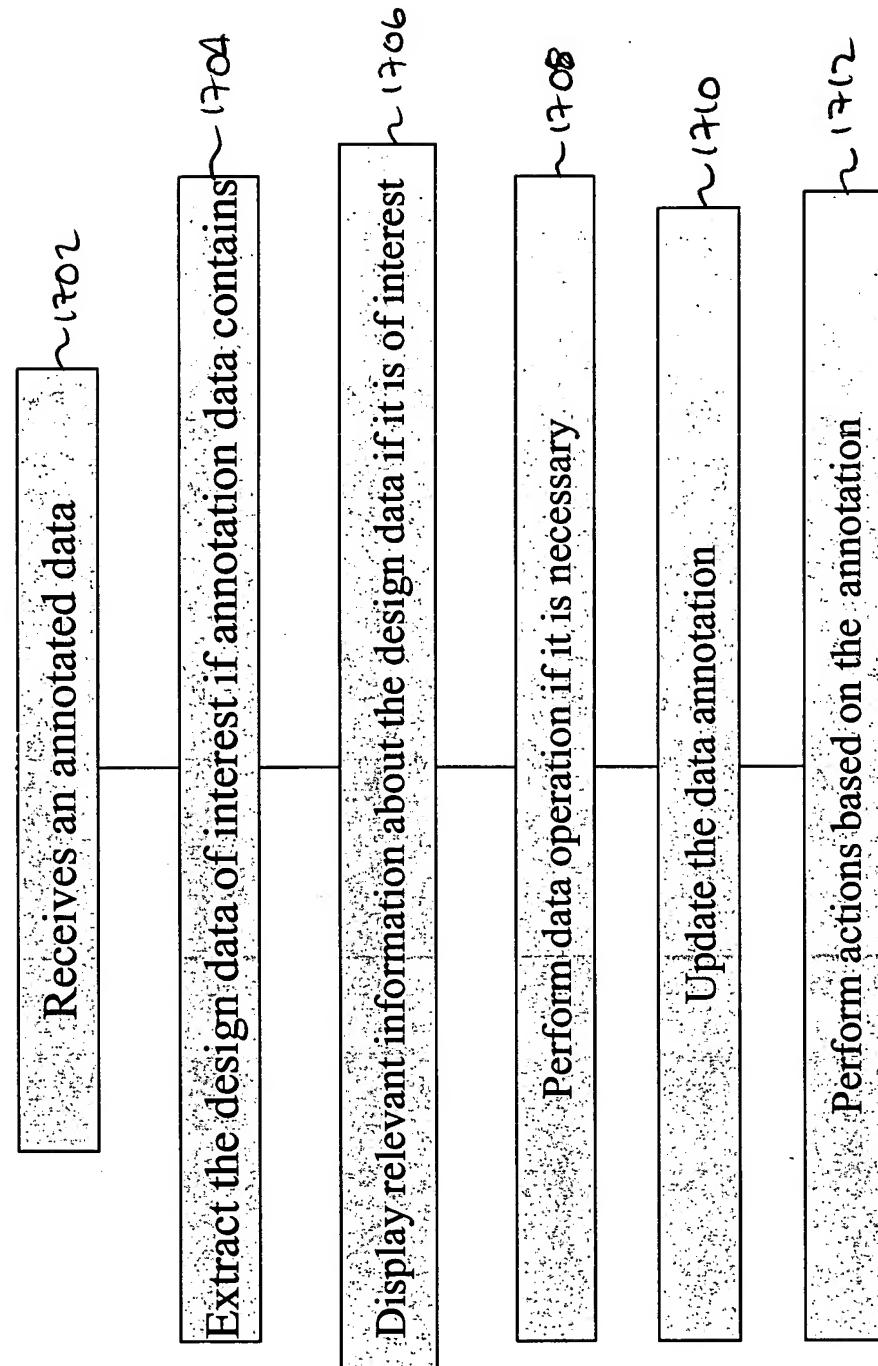


Fig. 17



FIG. 18A



FIG. 18B

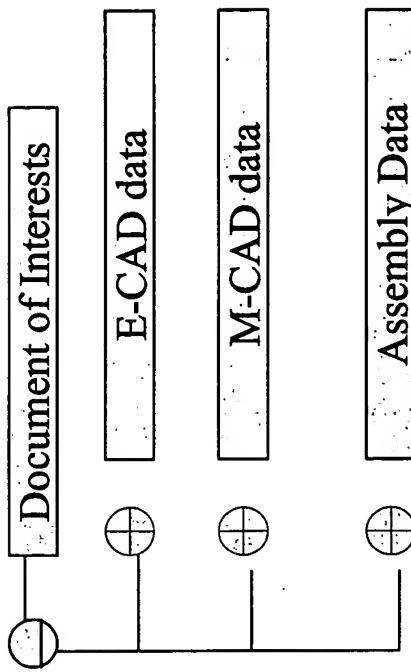


FIG. 18C

FIG. 18B

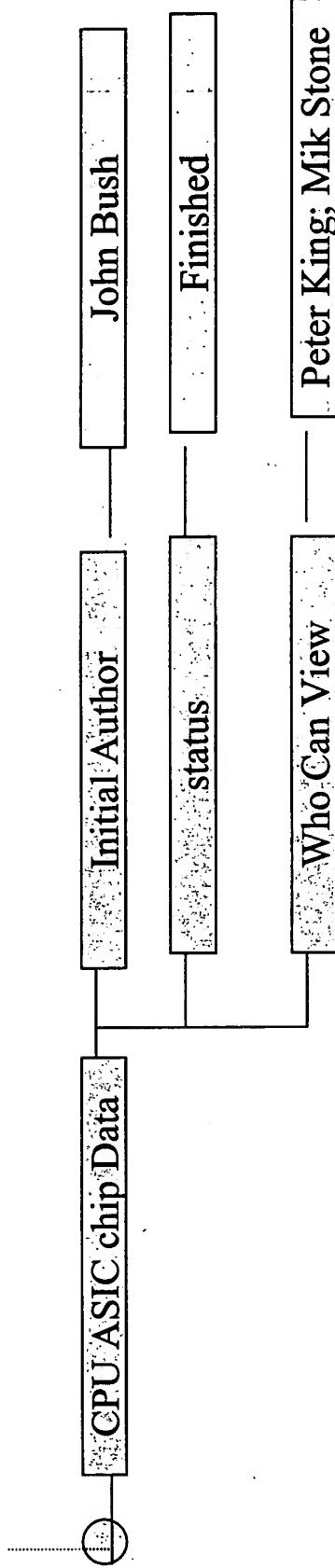
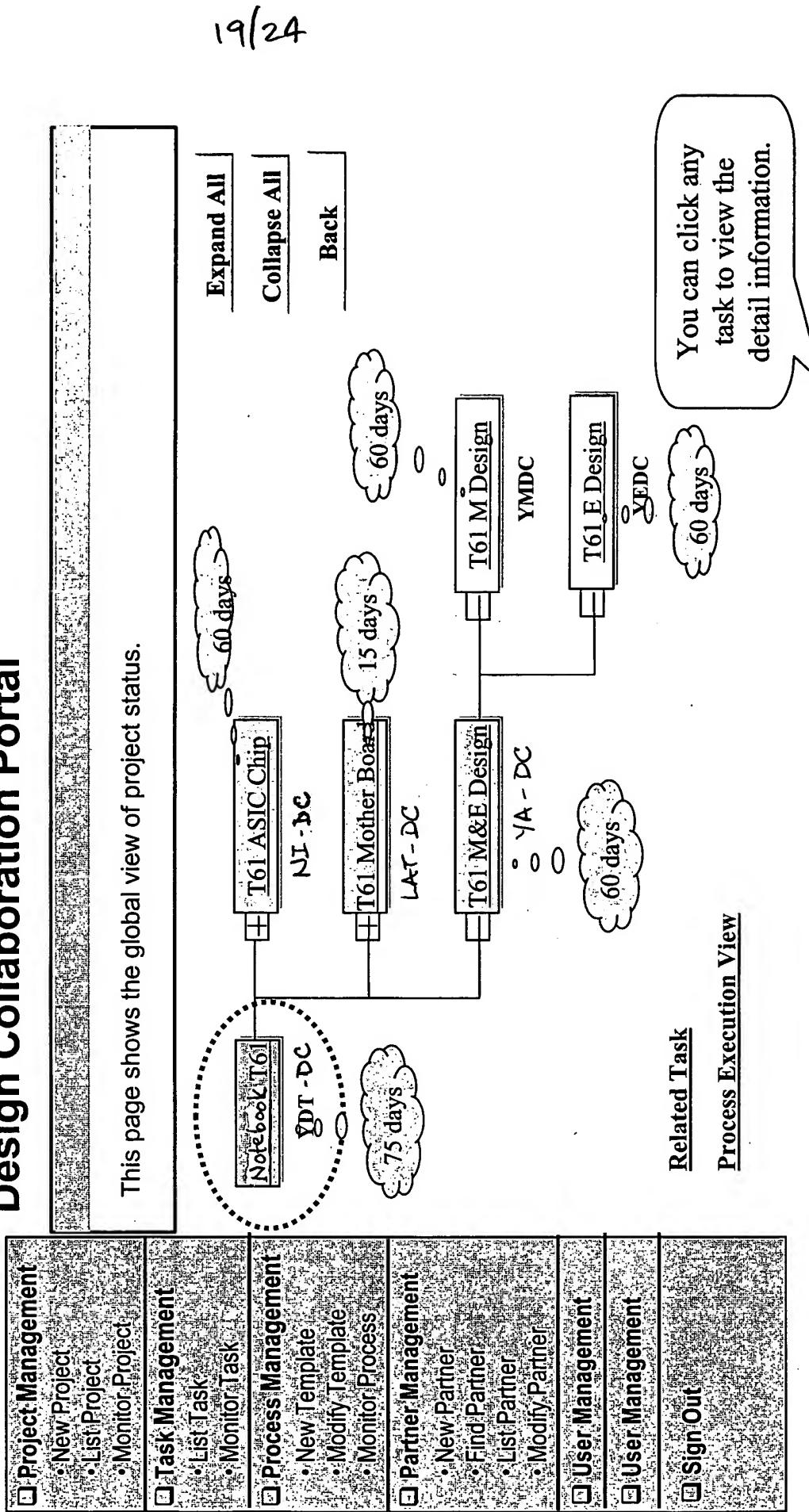


FIG. 18C

Fig. 19A

YDT-DC Project View

Design Collaboration Portal



Top

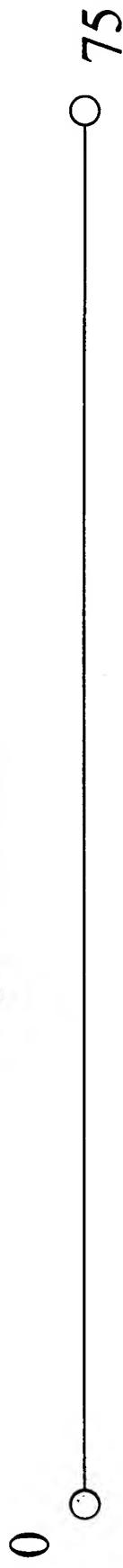


FIG. 19 B
Pyramid
Netbook T61

Bottom

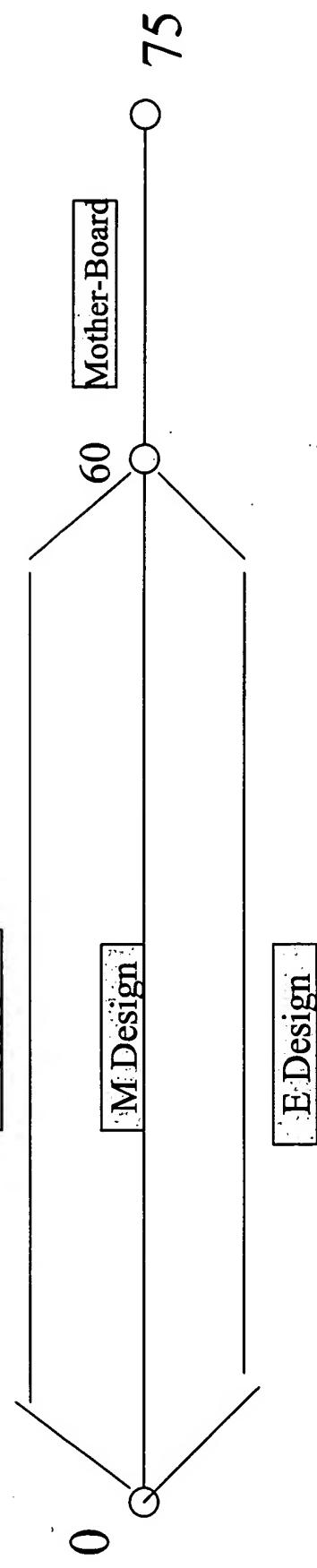


FIG. 19C

Offset Calculation

T61 M Design Offset: $T_{M\text{Design}} - 60$
YMDC

T61 E Design Offset: $T_{E\text{Design}} - 60$
YEDC

T61 M&E Design
YAE-DC

Offset: $\max\{T_{E\text{Design}} - 60, T_{M\text{Design}} - 60\}$

T61 Mother Board
YAE-DC

Offset: $\max\{T_{E\text{Design}} - 60, T_{M\text{Design}} - 60, T_{ASIC} - 60\} + T_{Board} - 15$

T61 ASIC Chip
YAE-DC

Offset: $T_{ASIC} - 60$

Notepad T61
YDR-DC

Offset: $\max\{T_{E\text{Design}} - 60, T_{M\text{Design}} - 60, T_{ASIC} - 60\} + T_{Board} - 15$

It must be calculated after all M&E, ASIC

At any time t, if T_{ASIC} etc. will take the value of t for the calculation

Fig. 19D

Checkpoint Calculation

$\boxed{\text{T61 M Design}}$
YMDC

$\boxed{\text{T61 E Design}}$
YEDC

$\boxed{\text{T61 M&E Design}}$
YAE-DC

$\boxed{\text{T61 Mother Board}}$
LAT-DC

$\boxed{\text{T61 ASIC Chip}}$
NIE-DC

$\boxed{\text{Nort8864T61}}$
YDT-DC

Same as Motherboard

It must be calculated after all M&E, ASIC

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$\boxed{\text{T}_M \text{ Design}}$ - 60 + 60

$\boxed{\text{T}_E \text{ Design}}$ - 60 + 60

$\max \{ \boxed{\text{T}_E \text{ Design}} - 60, \boxed{\text{T}_M \text{ Design}} - 60 \} + 60$

$(\boxed{\text{T}_{\text{Board}}} - 15) + \max \{ \boxed{\text{T}_E \text{ Design}} - 60, \boxed{\text{T}_M \text{ Design}} - 60, \boxed{\text{T}_{\text{ASIC}}} - 60 \} + 60$

Offset: $\boxed{\text{T}_{\text{ASIC}} - 60 + 60}$

At any time t, if $\boxed{\text{T}_{\text{ASIC}}}$ etc. will take the value of t for the calculation

FIG. 19E

Energy Calculation

```
0.5 * Sign[CheckPoint - BaseCheckPoint]  
* K  
* [CheckPoint - BaseCheckPoint]^2
```

Here K gives the importance of the process

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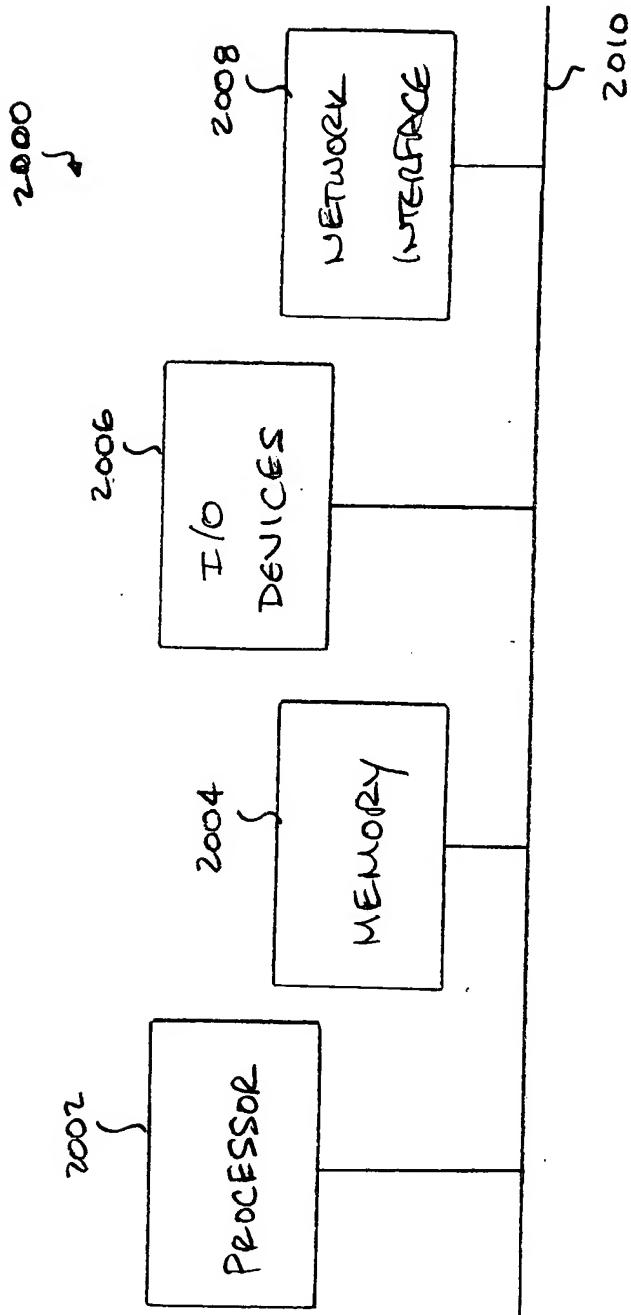


FIG 20